

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-607001 ✓	Application No. 10/054,179 ✓
<b>Information Disclosure Statement</b> by Applicant (Use several sheets if necessary)  (37 CFR §1.98(b))		✓ Applicant William R. Wheeler et al.	
		Filing Date January 17, 2002 ✓	Group Art Unit 2123 ✓ 2825

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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	2002/0049957	04/2002	Hosono et al.			RECEIVED JUN 12 2003 Technology Center 2100
	AB	6,233,723	05/2001	Pribetich			
	AC	5,828,581	10/1998	Matumura			
	AD	2001/0018758	08/2001	Tanaka et al.			
	AE	5,666,289	09/1997	Watkins			
	AF	6,066,179	05/2000	Allan			
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	AI	5,513,119	04/1996	Moore et al.			
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	AO	6,044,211	03/2000	Jain			
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	AZ	5,506,788	04/1996	Cheng et al.			
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Examiner Signature	Date Considered 29 Sept - 2004
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(37 CFR §1.98(b))		Filing Date January 17, 2002	

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	ADD	6,378,115	04/2002	Sakurai			JUN 12 2003
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	AFF	6,260,179	07/2001	Ohsawa et al.			Technology Center 2100
	AGG	6,117,183	09/2000	Teranishi et al.			
	AHH	5,889,677	03/1999	Yasuda et al.			
	AII	6,298,468	10/2001	Zhen			
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	AKK	6,480,985	11/2002	Reynolds et al.			
	ALL	6,519,755	02/2003	Anderson			
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	AOO	6,053,947	04/2000	Parson			
	APP	6,135,647	10/2000	Balakrishnan et al			
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	ATT	6,487,698	11/2002	Andreev et al.			
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	AVV	2002/0166100	11-2002	Meding			
	AWW	US 5,629,857	05-1997	Brennan			
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	AYY	6,401,230	06/2002	Ahanessians et al.			
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	ABBB						

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<b>U.S. Patent Documents</b>							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	ACCC						

<b>Foreign Patent Documents or Published Foreign Patent Applications</b>							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Yes      No
	ADDD						
	AEEE						
	AFFF						

<b>Other Documents (include Author, Title, Date, and Place of Publication)</b>		
Examiner Initial	Desig. ID	Document
	AGGG	Foley et al., "An Object Based Graphical User Interface for Power Systems", IEEE Transactions on Power Systems, Vol. 8, No. 1, February 1993, pp. 97-104.
	AHHH	Pedram et al., "Floorplanning with Pin assignment", 1990 IEEE International Conference on Computer-Aided Design, 11 November 1990, pp. 98-101.
	AIII	Renoir, HDL Design Datasheet, Mentor Graphics, 1-8, 1999.
	AJJJ	Mentor Graphics Corporation, Renoir™ With HDL2Graphics™, pp. 1-6, 1998, Oregon.
	AKKK	Mentor Graphics Corporation, Renoir HDL Design Datasheet, pp. 1-2, 1999, Oregon.
	ALLL	Computer Design, "AFTER HARD KNOCKS, CYCLE-BASED SIMULATORS STAND THEIR GROUND". <a href="http://www.computer-design.com/Editorial/1996/10/ASIC/after.html">http://www.computer-design.com/Editorial/1996/10/ASIC/after.html</a> , accessed on August 23, 2001, pages 1-5.
	AMMM	
	ANNN	
	AOOO	

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<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)  (37 CFR 1.98(b))				Applicant <b>William R. Wheeler et al.</b>	MAR 17 2004
		Filing Date <b>January 17, 2002</b>	Group Art Unit <b>2123 Technology Center 2100</b>		

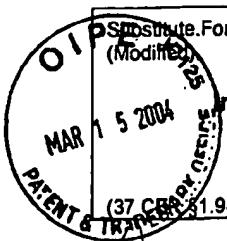
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William R. Wheeler et al.Filing Date  
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	AEE	5,937,190	08/1999	Gregory, et al.			
	AFF	5,974,242	10/1999	Damarla, et al.			
	AGG	6,077,304	06/2000	Kasuya			
	AHH	6,161,211	12/2000	Southgate			
	AII	6,178,541	01/2001	Joly, et al.			
	AJJ	6,208,954	03/2001	Houtchens			
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	AWW	6,438,729	08/2002	Ho			
	AXX	6,438,731	08/2002	Segal			
	AYY	6,440,780	08/2002	Kimura, et al.			
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	AAAA	6,477,688	11/2002	Wallace			
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	AEEE	6,519,742	02/2003	Falk			
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	AIII	6,574,787	06/2003	Anderson			
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	AOOO	2003/0004699	01/2003	Choi, et al.			
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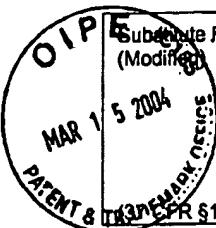
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	ARRR	0 404 482	12/1990	EPO			
	ASSS	0 433 066	06/1991	EPO			
	ATTT	0 720 233	07/1996	EPO			
	AUUU	0 901 088	03/1999	EPO			
	AVVV	1 065 611	01/2001	EPO			
	AWWW	58-060559	04/1983	Japan			
	AXXX	03-225523	10/1991	Japan			
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Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation
							Yes No
	AAAAA	2001-068994	03/2001	Japan			
	BBBBB	WO 98/37475	08/1998	WIPO			
	CCCCC	WO 98/55879	12/1998	WIPO			
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	AGGGG	Kutzschebauch, "Efficient logic optimization using regularity extraction", Proceedings of 2000 International Conference on Computer Design, September 17, 2000, pp. 487-493.
	AHHHH	Lahti, et al., "SADE: a Graphical Toll for VHDL-Based System Analysis", 1991 IEEE International Conference on Computer-Aided Design, November 11, 1991, pp. 262-265.
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	ALLLL	NN7807629, "Functional Oriented Symbolic Macromodeling Algorithm", IBM Technical Disclosure Bulletin, Vol. 21, No. 2, July 1978, pp. 629-631.
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	APPPP	Su, Stephen, "An Interactive Design Automation System", <i>Proceedings of the 10th Design Automation Workshop on Design Automation</i> , pp. 253-261, June 1973
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